



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

**NAKAJYO, Shinsuke et al.**

Group Art Unit: 2824

Serial No.: 09/686,958

Examiner: **Adam Pyonin**

Filed: **October 12, 2000**

P.T.O. Confirmation No.: 5708

For. **MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR DEVICE**

**LETTER RE DRAWINGS**

Commissioner for Patents  
Washington, D.C. 20231

*6th letter to  
the Draftsman*  
August 29, 2002

Dear Sir:

With the concurrence of the examiner, please amend the drawings of the above identified application as shown in red in the attached copies of Figures 1-5. The amendments involve deleting the Japanese characters. If any fees are necessitated by the this Letter, such may be charged to Deposit Account 1-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

Donald W. Hanson  
Attorney for Applicants  
Reg. No. 27,133

DWH/nk  
Atty. Docket No. **001344**  
Suite 1000, 1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



23850

PATENT TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

SEP - 6 2002

RECEIVED

Enclosure: Figures 1-5